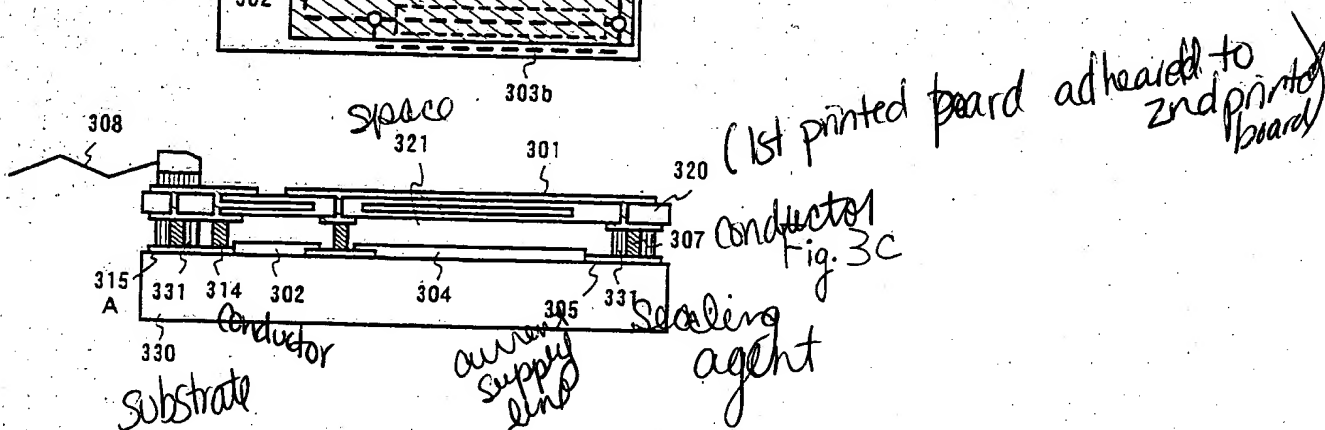
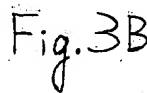
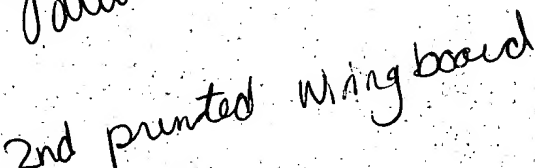
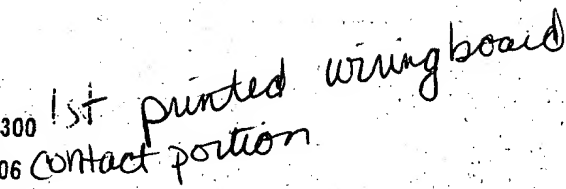
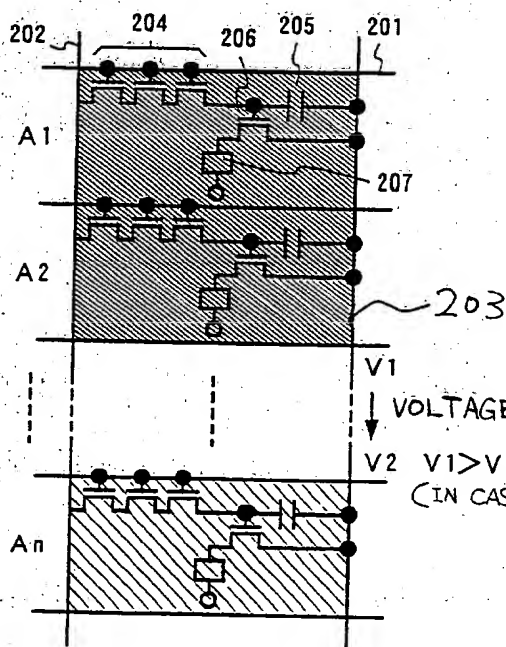
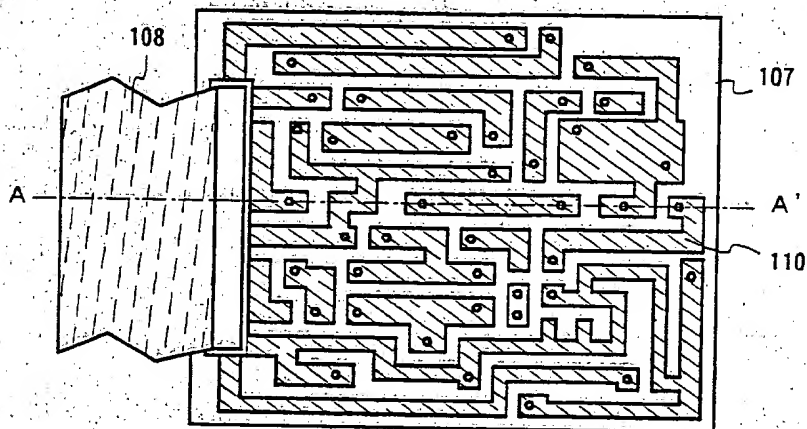
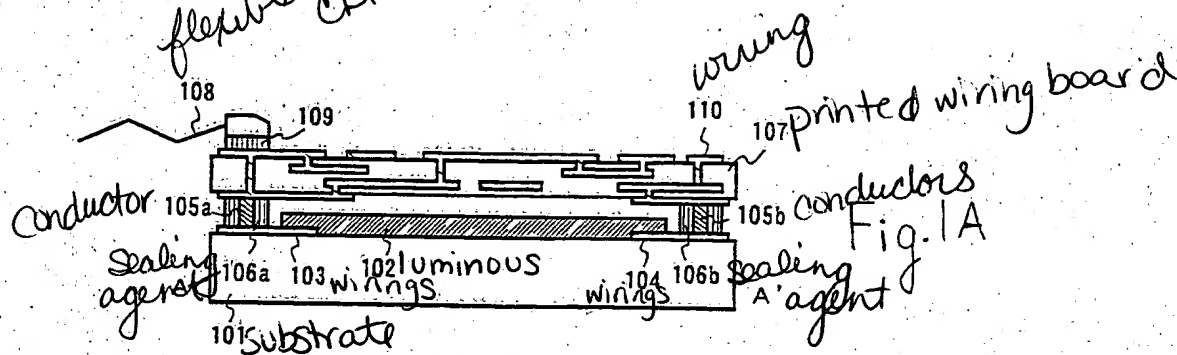


winning source driver skt  
gate driver skt

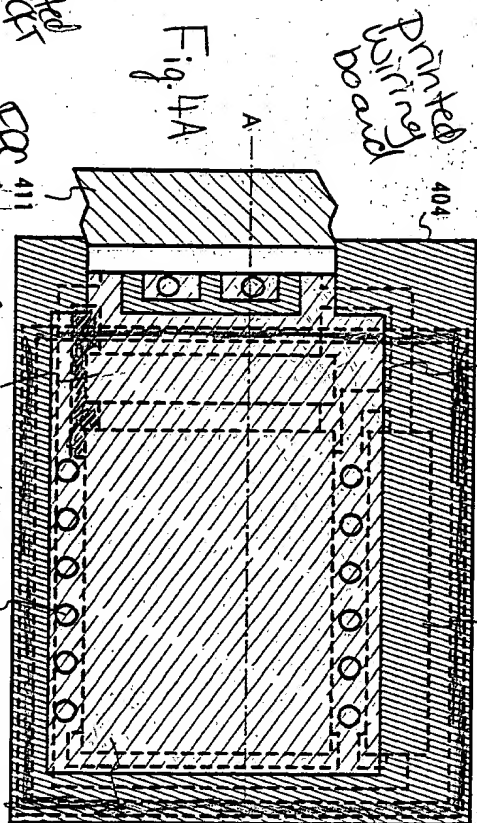


Multilayer wiring  
lamination wiring

105 → an isotropic conductive  
film  
conductive paste  
or bump



PWB wiring openings at



Printed wiring board

Fig. 4A

404 1st sealing member  
406 Pixel portion  
407  
408  
409 Contact portion  
410 solder  
411 Flexible printed circuit

connecting wires = current supply line

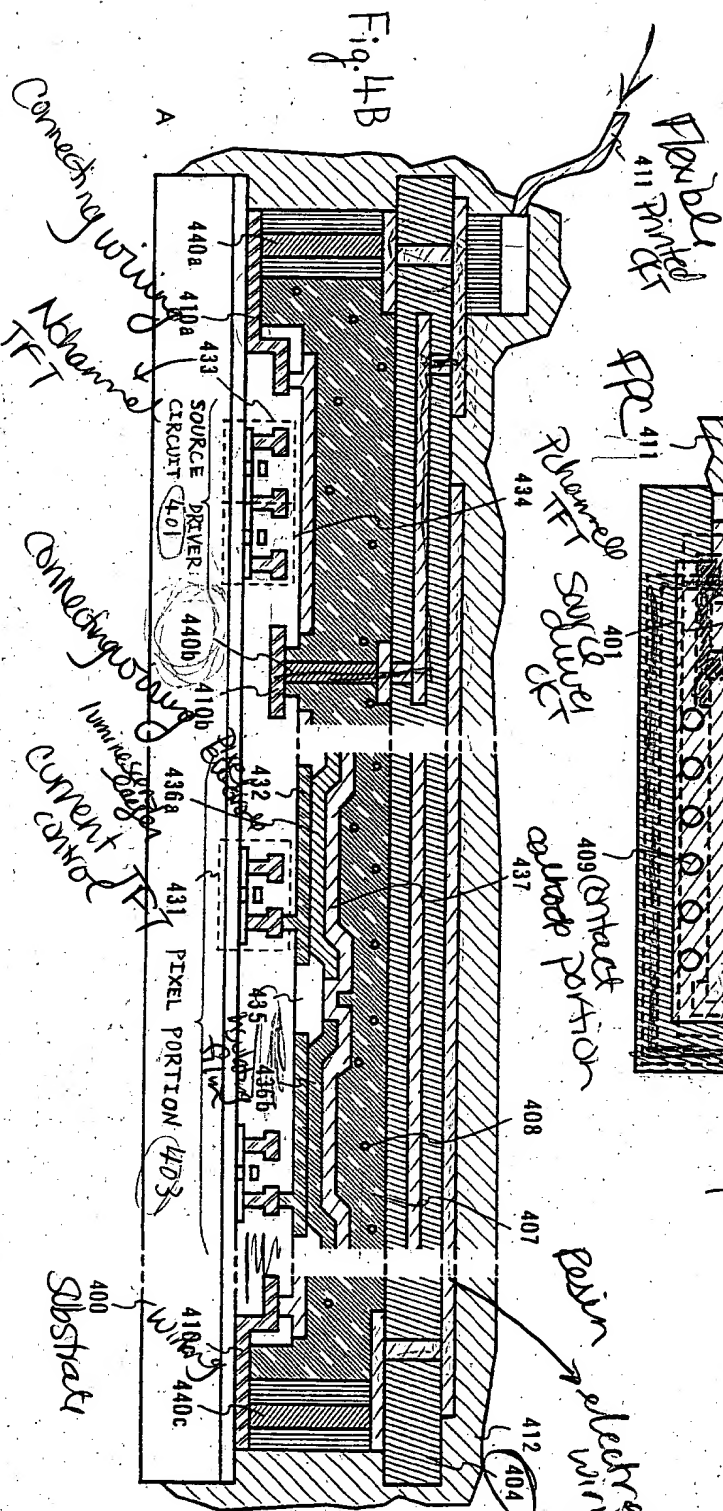


Fig. 4B

411 Flexible printed circuit  
412 substrate  
413 resin electrode  
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431 Pixel portion  
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440a  
440b SOURCE DRIVER CIRCUIT  
440c  
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Underlying support under support

Method  $\Rightarrow$  common wiring, electrically connected to connecting wires connecting wires electrically connected to PWB wiring  $\therefore$  connecting wires connected to PWB



